

An efficient inductor-capacitor-inductor-capacitor compensation topology for wireless power transfer system

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ABSTRACT

Wireless power transfer (WPT) systems provide a promising alternative for charging various applications, including electric vehicles (EVs), biomedical implants, smartphones, and network sensors. However, these systems often struggle to maintain high efficiency under varying loading and coupling conditions. This paper addresses these challenges by proposing a novel hybrid inductor-capacitor-inductor-capacitor (LC-LC) compensation topology. The proposed LC-LC topology is specifically designed to outperform conventional single-element compensation topologies, such as series-series (SS) and series-parallel (SP) configurations, by effectively reducing leakage inductance between coils. An analytical model of the LC-LC topology is developed and validated through simulations using Keysight advanced design system (ADS) software. The results demonstrate that the LC-LC topology not only achieves a peak efficiency of 99.6% under optimal conditions but also maintains superior performance compared to SS and SP topologies, with only a slight decrease to 93% efficiency observed at low load resistances. These findings highlight the potential of the LC-LC topology to significantly enhance WPT system efficiency across a range of operating conditions.

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1. INTRODUCTION

The wireless power transfer (WPT) system is the wireless transmission of energy that provides a compelling alternative to conventional charging systems [1], [2], finding applications for the charging of electric vehicles (EVs) [3], portable devices, including laptops and smartphones [4], as well as for powering biomedical implants [5]. Near-field electromagnetic induction techniques, such as inductive and magnetic resonance coupling (MRC) [6], significantly impact WPT system design, with MRC offering higher coupling efficiency when tuned to the same resonance frequency [7], [8]. The MRC technique is widely used in WPT systems, particularly for applications requiring minimal power, like charging handheld devices and sensors [9]. In the design process of a WPT system, different components, including coils [10], [11], compensation networks, and power converters (inverters and rectifiers) [4], [12], need to be considered, taking into account their application [4], [10], [12]. The WPT design aims for optimal energy transfer efficiency, load-independent output voltage and current, and a high-quality factor. Among these components of a WPT system, compensation networks play a critical role in maximizing power transfer efficiency (PTE), minimizing leakage inductance (resulting from magnetic field coupling), and capacitance (related to electric field coupling) generated by the coils and capacitor plates, respectively.

Compensation topologies in the WPT system, include single-resonant/single-element and hybrid/multi-element structures, with four fundamental network topologies: series-series (SS), series-parallel (SP), parallel-series, and parallel-parallel [13]-[15]. On the other hand, hybrid compensation topologies employ multiple capacitors and inductors as compensation elements. Extensive research has been conducted on both single and multi-element compensation topologies including a comparative study between single-element SS and SP topologies aimed at optimizing EV charging [16], as well as a study by Zhang *et al.* [17] to determine load-independent voltage transfer ratios and optimal maximum efficiency for single element topologies. Similarly, loss analysis was conducted on a low-power, high-frequency WPT system with SS topology, achieving 2.93 W peak output power and 60% PTE [18]. In terms of the hybrid /multi-element compensation topologies, an LCC-LCL hybrid compensation topology was investigated and compared with double-sided LCL and LCL-LCC topologies for performance optimization [19]. The investigation reveals the proposed LCC-LCL topology achieved 94% efficiency with an output power of 111 W as compared to 90% and 88% efficiency with 104W and 99W output power for DS-LCL and LCL-LCC topologies, respectively. An LC-S compensated WPT system with a 2 W output power and 84.4% efficiency was developed for implantable pacemaker charging [20].

The air gap between the transmitting and receiving coils causes leakage inductance in a simple WPT system without resonance; the leakage increases with the increase in the distance between the coils [21]. To mitigate this leakage, capacitors are often introduced [22]. The resonant tank formed by capacitors and inductors minimizes power leakage by ensuring the transmitter and receiver resonate at the same frequency, thereby improving PTE and minimizing current flow [23]. Achieving high PTE remains a key goal, regardless of load resistance and coupling coefficient variations. Series-compensated SS and SP topologies frequently struggle to achieve high performance, particularly under changing load and coupling conditions. Analysis of the SS and SP compensation topologies reveals that the variations in loading and coupling conditions, as well as changes in frequency, quality factor, and coil design, all have an impact on PTE. As a result, this paper proposes a hybrid LC-LC compensation topology and compares it to the series-compensated SS and SP compensation topologies. The proposed LC-LC topology provides several advantages, including a high coupling coefficient and load-independent output characteristics. It also offers high PTE across varying load resistances and coupling coefficients. The rest of the paper is organized as: section 2 describes the proposed LC-LC compensation topology, including the circuit model and design analysis. Section 3 presents the simulation results and compares the proposed topology to other compensation topologies. Finally, section 4 summarizes the work and proposes future research directions in this area.

2. METHOD

This paper proposes an LC-LC compensation topology (Figure 1(a)), which consists of a series compensation capacitor with a compensation inductor on both the primary and secondary circuits. The proposed compensation topology was compared to the series compensated SS and SP compensation topologies (Figures 1(b) and (c)). L_1 and L_2 represent the self-inductances of the transmitter and receiver coils, respectively, while C_1 and C_2 represent the transmitter and receiver compensation capacitors. The load resistance is denoted by R_L , the input resistance of the primary side is denoted by R_{in} , and the input voltage source is denoted by V_s . L_P and L_S are the additional compensation inductors added to the primary and secondary sides, respectively. Figure 2 represents the schematic diagram for the transmitter and receiver's coil dimensions, such as transmission distance d , the radius of wire R_i , the radius of coil r_i , and the number of turns N_i .

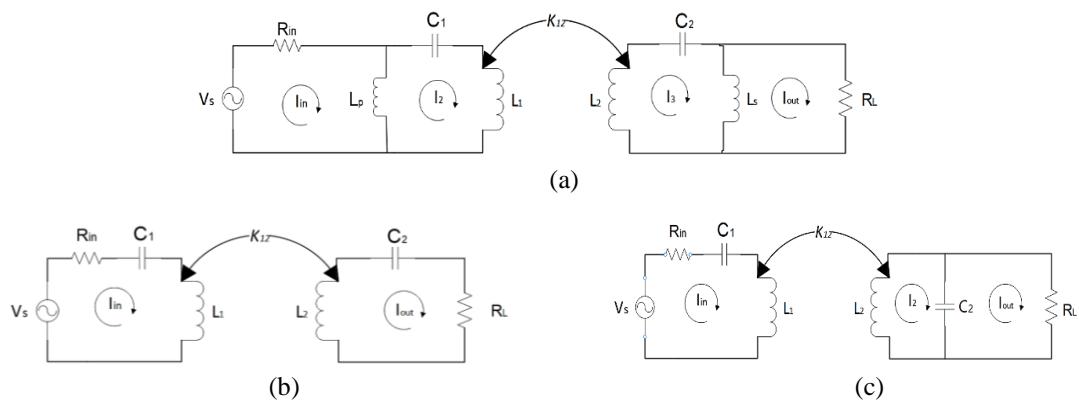


Figure 1. Schematic diagrams for the; (a) LC-LC, (b) SS, and (c) SP topologies

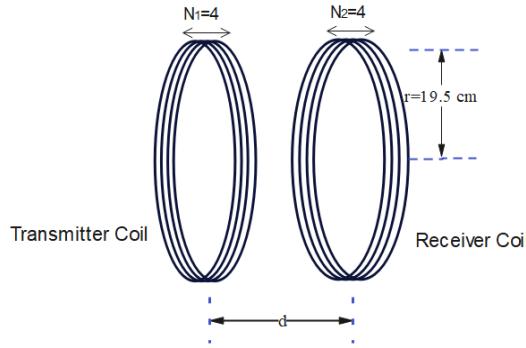


Figure 2. Schematic diagram representing the coil's structure

Self-inductance L_i of coil i ($i=1, 2$) is determined by the number of turns N_i , the radius of the wire R_i and the radius r_i of the coil i [7].

$$L_i = N_i^2 \mu_0 \mu_r r_i \left[\ln \left(\frac{8r_i}{R_i} \right) - 2 \right], \forall i = 1, 2 \quad (1)$$

where, μ_0 and μ_r are the freespace permeability and relative permeability, respectively. Mutual inductance M_{12} between the transmitter and receiver coils can then be determined using the Neumann formula for circular coils with a gap distance of d between the transmitter and receiver coils and an angle θ between the coils' axis [24],

$$M_{12} = \frac{2\mu_0 N_1 N_2 \sqrt{a+b}}{b} \left[\left(1 - \frac{\beta^2}{2} \right) K(\beta) - E(\beta) \right] \quad (2)$$

where

$$a = \frac{(r_1)^2 + (r_2)^2 + d^2}{(r_1)^2 (r_2)^2}; b = \frac{2}{(r_1)(r_2)}; \beta = \sqrt{\frac{2b}{a+b}}$$

$$K(\beta) = \int_0^{\pi/2} \left(\frac{d\theta}{\sqrt{1 - \beta^2 \sin^2 \theta}} \right); E(\beta) = \int_0^{\pi/2} \left(\sqrt{1 - \beta^2 \sin^2 \theta} d\theta \right)$$

The rate at which the two inductive coils are magnetically coupled with each other is represented by the coupling coefficient k_{12} between the transmitter and receiver coils. A high coupling coefficient generally results in a higher PTE, and vice versa. The coupling coefficient k_{12} is defined as the mutual inductance divided by the product of the self-inductance of two coils [25].

$$k_{12} = \frac{M_{12}}{(\sqrt{L_1 L_2})}, 0 < k < 1 \quad (3)$$

For the proposed compensation network in Figure 1(a), the reactance of the circuit and the intended resonance frequency f_0 can be used to estimate the values of the compensating capacitors, C_1 and C_2 ,

$$C_1 = \frac{1}{(2\pi f_0)^2} \left(\frac{1}{L_P + L_1} \right); C_2 = \frac{1}{(2\pi f_0)^2} \left(\frac{1}{L_S + L_2} \right) \quad (4)$$

Kirchhoff's voltage law can be utilized on the transmitter and receiver circuits of the proposed compensation network, to establish expressions for power delivered (P_{in}) and power dissipated (P_{out}). Finally, the WPT efficiency (η_{LC-LC}) from source to load can be calculated using P_{in} and P_{out} .

$$\eta_{LC-LC} = \frac{P_{out}}{P_{in}} = \frac{R_L |I_{out}|^2}{|V_s I_{in}|} \quad (5)$$

Given that P_{in} and P_{out} by the load can be derived as (6) and (7):

$$P_{in} = |V_s I_{in}| = [(R_{in} + \omega L_p) |I_{in}| - \omega L_p |I_2|] |I_{in}| \quad (6)$$

$$P_{\text{out}} = \frac{(\omega^3 k_{12} L_p L_s |I_{in}|)^2 L_1 L_2 R_L}{\left[(\omega L_s + R_L) \left\{ \left(\omega L_s + \omega L_2 - \frac{1}{\omega C_2} \right) \left(\omega L_p + \omega L_1 - \frac{1}{\omega C_1} \right) - (\omega k_{12})^2 L_1 L_2 \right\} - (\omega L_s)^2 \left(\omega L_p + \omega L_1 - \frac{1}{\omega C_1} \right) \right]^2} \quad (7)$$

PTE (η_{LC-LC}) for the proposed compensation topology can be expressed as (8):

$$\eta_{LC-LC} = \frac{(\omega^3 k_{12} L_p L_s)^2 L_1 L_2 R_L |I_{in}|}{\left[(R_{in} + \omega L_p) |I_{in}| - \omega L_p |I_{2L}| \right] \left[\left[(\omega L_s + R_L) \left\{ \left(\omega L_s + \omega L_2 - \frac{1}{\omega C_2} \right) \left(\omega L_p + \omega L_1 - \frac{1}{\omega C_1} \right) - (\omega k_{12})^2 L_1 L_2 \right\} - (\omega L_s)^2 \left(\omega L_p + \omega L_1 - \frac{1}{\omega C_1} \right) \right]^2 \right]} \quad (8)$$

η_{LC-LC} can be used to analyze the proposed compensation network, and its performance, particular, in comparison to the series compensated SS and SP topologies.

3. RESULTS AND DISCUSSION

The proposed hybrid LC-LC and series compensated SS and SP compensation topologies were modelled and simulated in keysight advanced design system (ADS) software. Table 1 lists the values of the parameters used for the simulation of the circuits, such as the coil's dimensions, input voltage and resistance. Based on these parameters, self-inductance was determined to be $L_1=L_2=18.3 \mu H$. Compensation capacitor values for the SS and SP compensation topologies were determined to be $C_1=C_2=62 nF$. For the proposed hybrid LC-LC compensation topology, primary and secondary compensation capacitor values $C_1=C_2=25 nF$ were taken based on primary and secondary compensation inductor values $L_p=L_s=26.5 \mu H$.

Table 1. Important parameters of the circuits used in the simulation

Parameter	Value
Radius r_i of coil i , $\forall i = 1, 2$	19.5 cm
Radius of wire R	0.1 cm
Permeability of free space μ_0	$4\pi \times 10^{-7} N/A^2$
Relative permeability μ_r	0.9999
Number of turns N_i of coil i , $\forall i = 1, 2$	4
Resonance frequency, f_0	150 kHz
Input resistance R_{in}	1 Ω
Input voltage V_s	21.2 Vrms

Table 2 summarizes the values for the various circuit components stated above. The purpose of the input resistance R_{in} on the primary side is to limit the leakage current and hence reduce the VA rating of the transmitter. At resonance, however, the compensation capacitor with its capacitive reactive power cancels out the combined inductive power produced by the compensation inductor (L_p) together with the self-inductance of coil 1 (L_1).

Table 2. Summary of the derived components values of different topologies

Parameters	Single-element network topologies		Proposed topology
	SS	SP	
Self-inductance $L_1=L_2$		18.3 μH	
Compensation inductor L_p	N/A		26.5 μH
Compensation inductor L_s	N/A		26.5 μH
Compensation capacitor C_1	62 nF		25 nF
Compensation capacitor C_2	62 nF		25 nF

3.1. Power characteristics of compensation topologies

Figures 3(a)-(f) demonstrates the relationship between input/output power (P_{in}/P_{out}) and coupling coefficient (k_{12}) for the proposed LC-LC, SS, and SP compensation topologies, with LC-LC topology showing unique patterns with both input and output power exhibiting similar trends, as can be seen in Figures 3(a) and (b). Input and output power generally increases with k_{12} , however, as R_L increases, the power curve peaks and subsequently declines with increasing k_{12} . For both the SS and SP topologies, input power trends downward with k_{12} . Specifically, SS exhibits a trend where higher R_L values are associated with higher input power, however, the converse is observed with SP, where lower R_L values lead to higher input power (Figures 3(c) and (e)). P_{out} for the SS topology initially rises before peaking and subsequently, declines with

an increase in k_{12} ; with the power curve reaching its peak earlier at lower R_L (Figure 3(d)). In contrast, the P_{out} generally declines in SP topology with k_{12} , yet it increases initially at low R_L before peaking (Figure 3(f)). Additionally, the P_{out} for the SS and SP topologies is significantly lower than the P_{in} , signifying lower efficiency. The LC-LC topology shows lower input and output power than SS and SP topologies, indicating distinct performance and efficiency dynamics in response to coupling coefficient and load resistance variations.

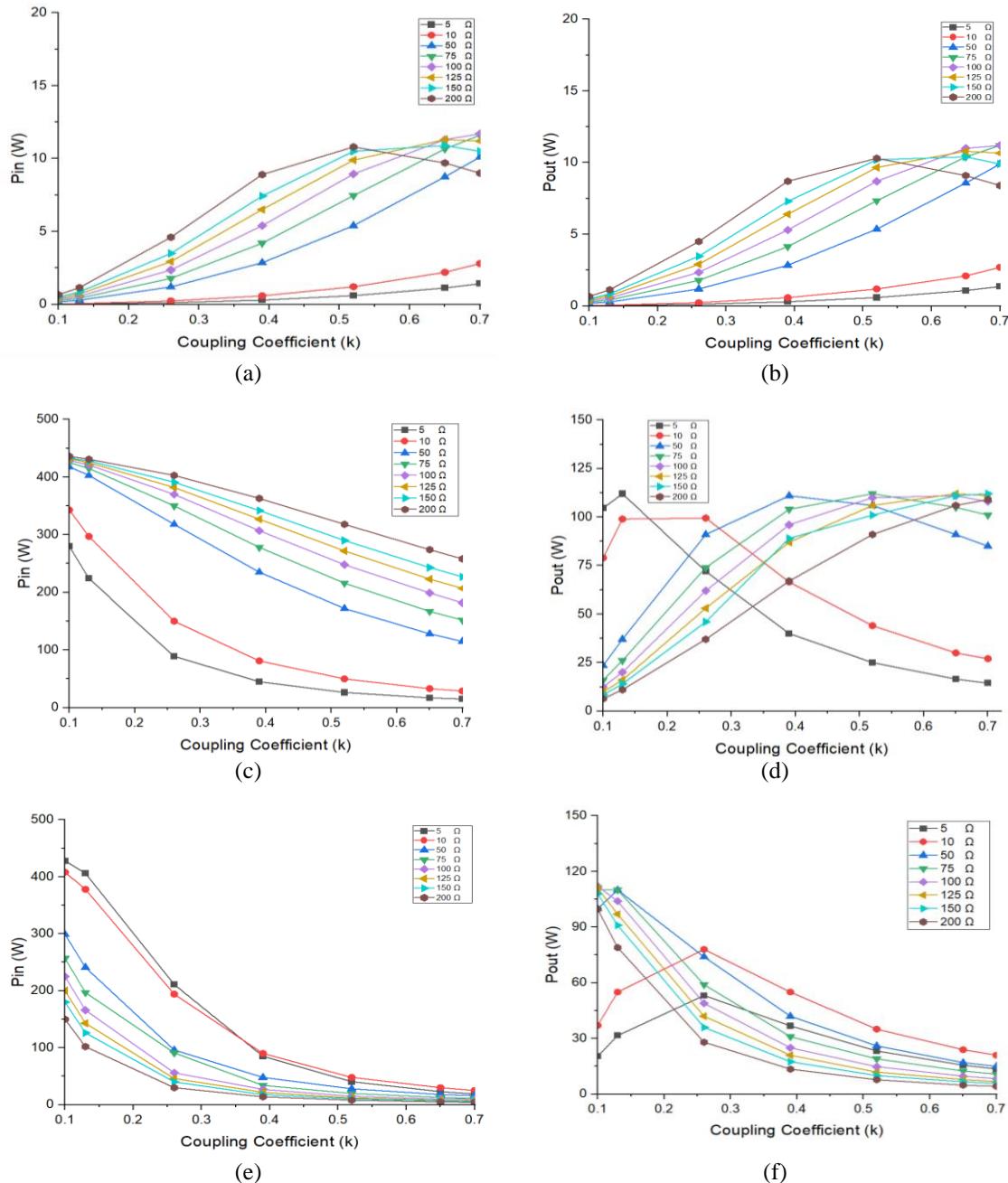


Figure 3. Input/output power for different compensation topologies: (a) input power LC-LC, (b) output power LC-LC, (c) input power SS, (d) output power SS, (e) input power SP topologies, and (f) output power SP topologies

3.2. Current characteristics of compensation topologies

Figure 4 shows the relationship between input/output current (I_{in}/I_{out}) and coupling coefficient (k_{12}) across varying load resistance (R_L) for the proposed LC-LC, SS, and SP compensation topologies. Both the input and output currents of the LC-LC generally increase with k_{12} , as can be seen in Figures 4(a) and (b); with

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the input current curve peaking, particularly across large R_L . Whilst large R_L values are associated with high input current, the converse is true for output current, where small R_L generally results in higher output current. I_{out} of SS does not vary much and remains constant with varying k_{12} values (Figure 4(d)), except small R_L values where I_{out} initially increases before peaking and subsequent decline is observed. However, the I_{out} for SP generally decreases with k_{12} (Figure 4(f)). However, I_{out} initially increases across small R_L values, for SP before peaking, followed by a subsequent decline as k_{12} increases. The I_{in} of both SS and SP topologies exhibits a declining trend with k_{12} (Figures 4(c) and (e)). Specifically, large R_L values are associated with higher input current in SS topology, whilst small R_L values are associated with higher input current in SP topology.

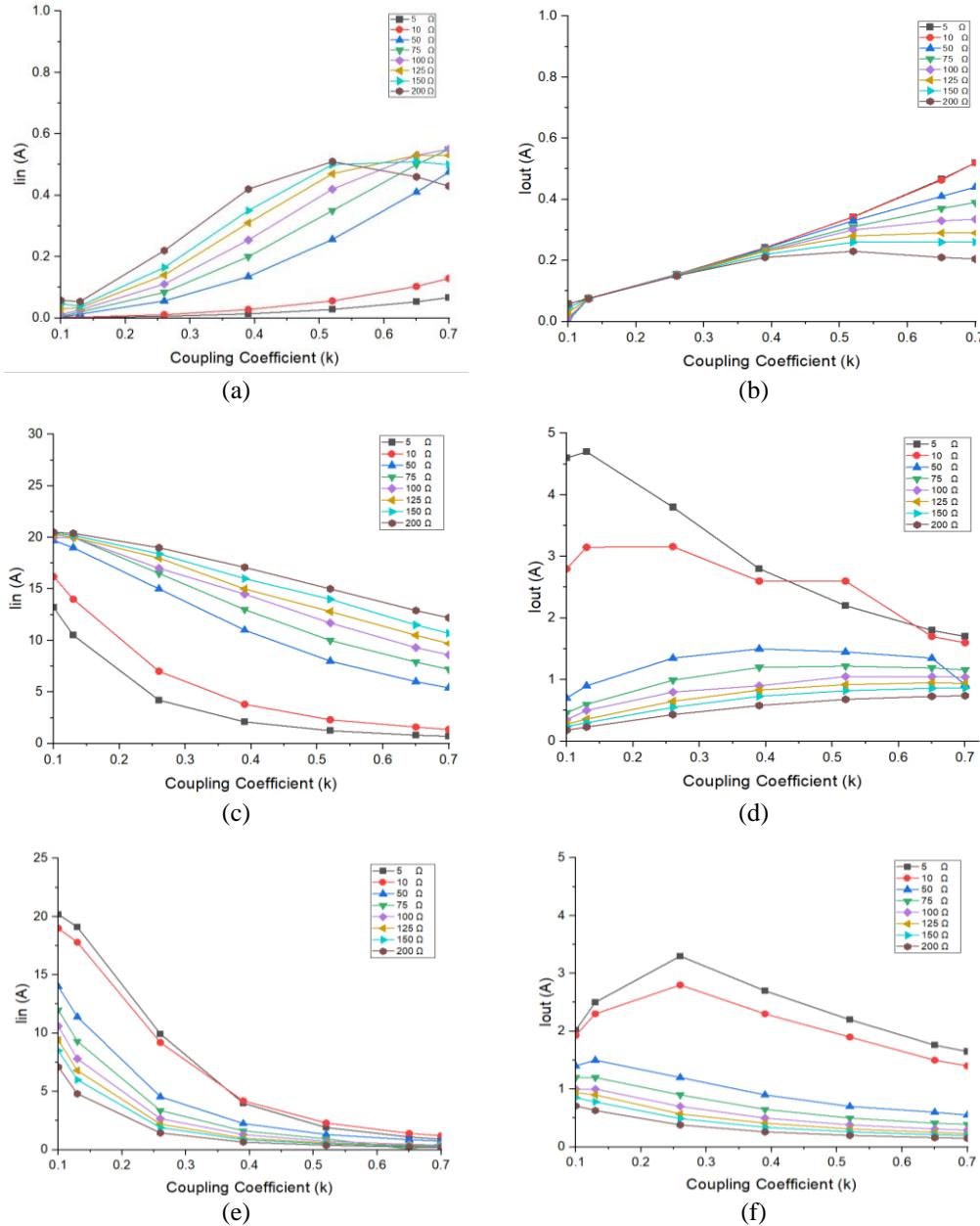


Figure 4. Input/output current for different compensation topologies: (a) input current LC-LC, (b) output current LC-LC, (c) input current SS, (d) output current SS, (e) input current SP topologies, and (f) output current SP topologies

3.3. Voltage characteristics of compensation topologies

Figure 5 shows the relationship between the output voltage (V_{out}) and coupling coefficient (k_{12}) across varying load resistance (R_L). V_{out} for both the proposed LC-LC and SS topologies generally increases with k_{12}

(Figures 5(a) and (b)), with large R_L associated with higher output voltage. An exception to this trend is in the case of small R_L for SS, whereby a slight decreasing trend in V_{out} is observed with k_{12} . This contrasts with the V_{out} trend of SP, with output voltage generally decreasing with k_{12} , as shown in Figure 5(c), except across small load resistance, where a slight peak is observed despite output voltage remaining at approximately the same value. Large load resistance values are associated with a higher output voltage for all three topologies.

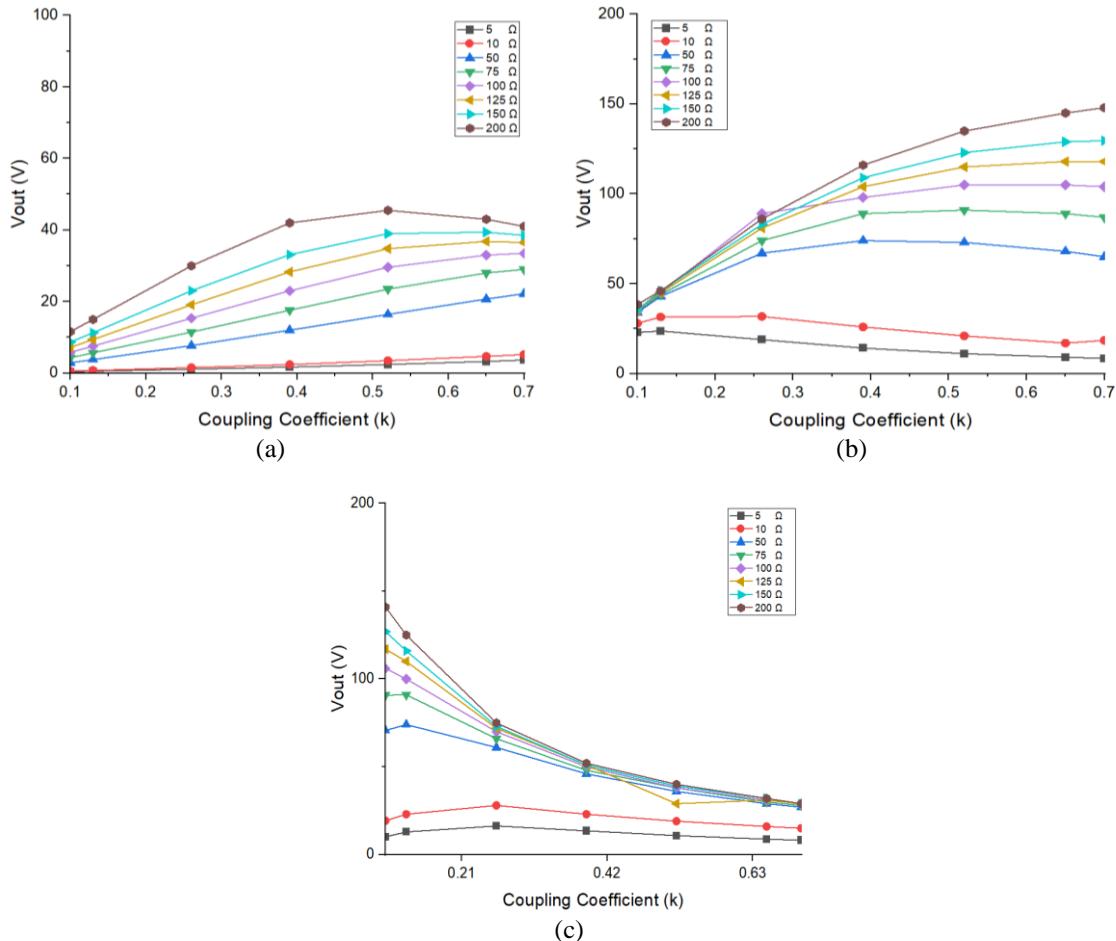


Figure 5. Output voltage for; (a) LC-LC, (b) SS, and (c) SP topologies

3.4. Power transfer efficiency analysis

Figure 6 shows the relationship between the PTE and coupling coefficient (k_{12}) across varying load resistance (R_L). The proposed LC-LC topology in Figure 6(a) shows only a slight decrease in efficiency, particularly across large load resistance values, yet maintains high efficiency across most R_L ranges. Notably, with a coupling coefficient of $k_{12} \leq 0.39$, the efficiency of the proposed LC-LC topology is above 99%, irrespective of the load resistance values. Across a relatively large load resistance value of $R_L=200 \Omega$, efficiency reduces to 93% as the coupling coefficient increases. A peak efficiency of 99.6% is achievable with LC-LC across $R_L=80 \Omega$ and $K_{12}=0.1$. On the other hand, both the SS and SP topologies exhibit increased efficiency with a higher coupling coefficient (Figures 6(b) and (c)), achieving maximum efficiencies of 97% and 98% at specific conditions only; for SS and SP with $k_{12}=0.7$, across $R_L=5 \Omega$, and $R_L=200 \Omega$, respectively, with performance reducing considerably especially at lower coupling coefficients. Efficiency trends vary across load resistance, with higher efficiency at lower R_L values for SS and higher R_L values for SP, while LC-LC efficiency remains stable, reaching 99.6%. Notably, the lowest efficiency of 93% for the LC-LC topology at $R_L=200 \Omega$ and $K_{12}=0.7$ represents a modest reduction from its peak. The LC-LC topology exhibits superior efficiency, due to its simplified circuit design, which minimizes losses, excluding inverter and rectifier losses.

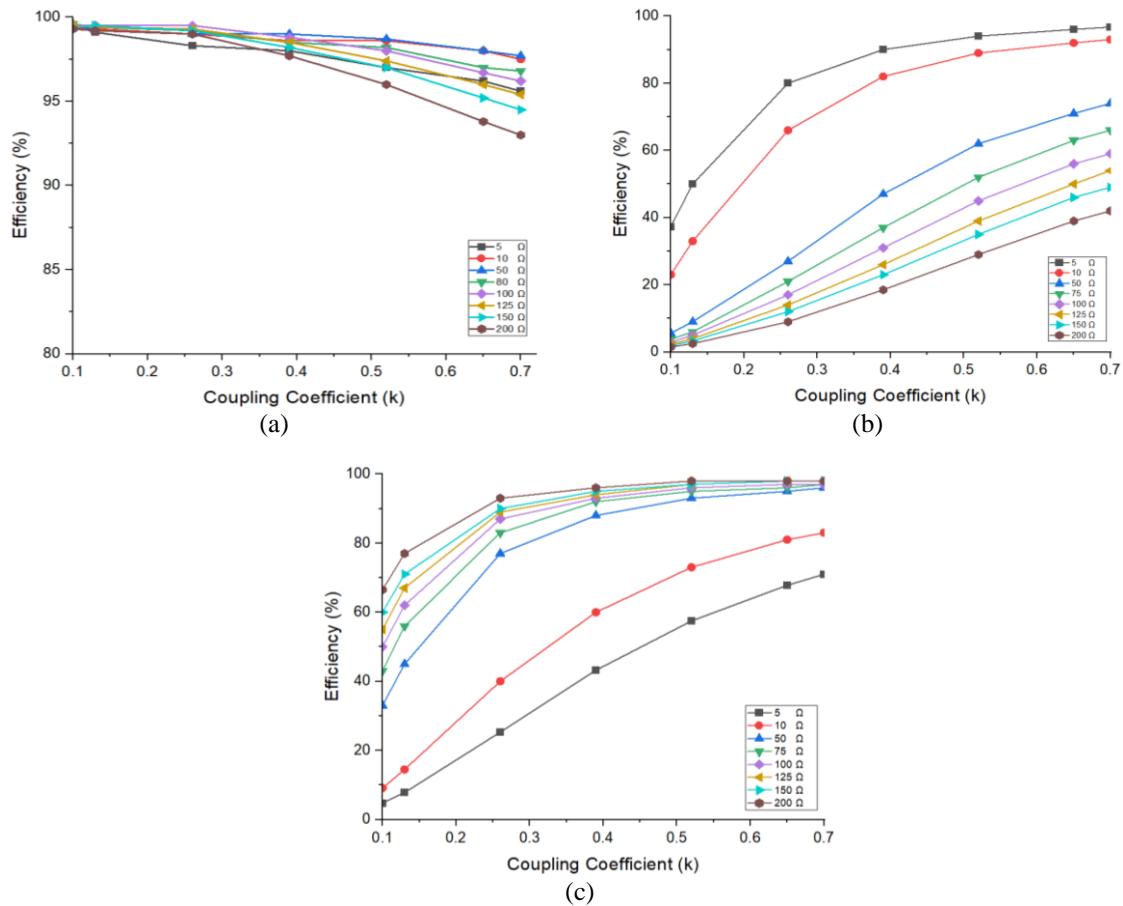


Figure 6. PTE for; (a) LC-LC, (b) SS, and (c) SP topologies

3.5. Frequency response analysis

Figure 7 depicts the relationship between efficiency and frequency for the proposed LC-LC compensation topology at various load resistances. The LC-LC topology exhibits the frequency splitting phenomenon at large load resistance values ($R_L=100 \Omega$, $R_L=200 \Omega$ (Figures 7(a) and (b)), especially when the coupling coefficient (k_{12}) is increased. At $k_{12}=0.7$, the resonance frequency splits into two halves, with low and high-frequency peaks at 132 and 180 kHz, respectively, for $R_L=200 \Omega$ (Figure 7(a)). It is obvious from the Figure 7(b) that the frequency splitting decreases as load resistance decreases. For instance, at $R_L=100 \Omega$, the efficiency reduction is small compared to the reduction in efficiency at $R_L=200 \Omega$. In Figure 7(b), the frequency curve splits into two halves at $k_{12}=0.7$, with low and high-frequency peaks at 135 kHz and 175 kHz, respectively. The frequency splitting in the proposed LC-LC topology is not observed at small load resistances (Figures 7(c) and (d)), indicating that the resonance frequency is independent of the coupling coefficient. The frequency splitting partly explains the observed reduction in PTE at large load resistance values as the coupling coefficient is increased in Figure 6.

The analysis highlights that the LC-LC topology exhibits load-independent output current characteristics, particularly at a low coupling coefficient of $k_{12} \leq 0.4$. On the other hand, both the SS and SP topologies exhibit load-independent output voltage characteristics at specific conditions: for SS, load-independent output voltage characteristics are observed at $k_{12} \leq 0.2$ and across $R_L \geq 50 \Omega$, whilst for SP, at $k_{12} \geq 0.4$ and across $R_L \geq 50 \Omega$. The proposed LC-LC topology is suitable for applications requiring stable current, like LED lighting, due to its consistent luminance requirement [26]. On the other hand, SS and SP topologies are ideal for battery charging due to their load-independent output voltage characteristics, allowing constant voltage, particularly within the specific k_{12} and R_L parameters. The analysis further indicates that both SS and SP topologies have high input current and power, resulting in higher volt-ampere (VA) ratings and leading to a significant power loss in the WPT system, which is evident from the efficiencies of the SS and SP topologies. The proposed LC-LC topology, with more reactive elements, demonstrates higher PTE, thereby minimizing power loss and subsequently enhancing PTE. This makes the LC-LC topology especially valuable in applications sensitive to input power variations. These findings also imply that strongly coupled WPT systems,

i.e., strong coupling coefficient, strictly aligned coils and a short transmitter-to-receiver distance are better suited for the SS and SP compensation topologies. On the other hand, the proposed LC-LC topology performs exceptionally well in both strongly coupled and loosely coupled WPT systems; demonstrating efficiency of above 98% and 93%, in a strongly coupled and weakly coupled WPT system. The proposed topology maintains superior efficiency across different transmission distances, distinguishing it from SS and SP topologies. However, it's less suitable for high power applications, but rather more suitable for medium power applications, including the charging of portable devices such as laptops and smartphones with power requirements ranging from a few to 100 watts.

Table 3 compares the proposed LC-LC topology with other compensation topologies in the literature, in terms of efficiencies and other relevant parameters, highlighting its competitive performance. Despite a lower output power of only 12 W, LC-LC is extremely efficient; capable of reaching 99.6% efficiency and the lowest efficiency of 93%, focusing on efficiency over maximum transfer of power. This contrasts with SS and SP topologies with peak output powers of 120 W and 125 W, respectively, and other hybrid topologies, such as the LC-S and LCC-LCL topologies, exhibiting peak output powers of 2 W and 111 W with input voltages of 4.9 V and 42 V, respectively. The higher output power of the series compensated SS and SP topologies is attributed to their higher VA rating at the expense of efficiency. In addition, the proposed LC-LC hybrid topology, designed for low power applications, outperforms single element and hybrid compensation topologies in terms of efficiency, despite its low output power.

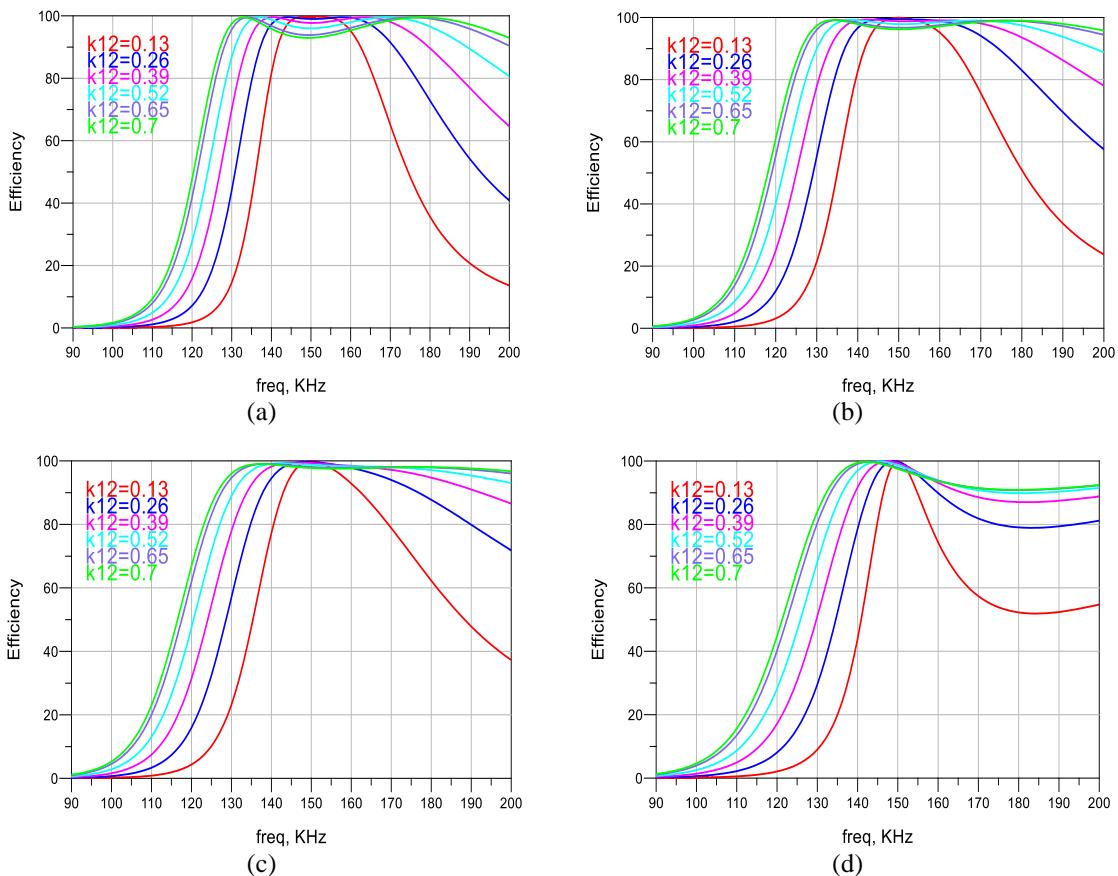


Figure 7. PTE vs frequency for; (a) $R_L=200 \Omega$, (b) $R_L=100 \Omega$, (c) $R_L=50 \Omega$, and (d) $R_L=10 \Omega$

Table 3. PTE and corresponding parameters of the different compensation topologies

Parameters	LC-LC	SS	SP	LC-S [20]	LCC-LCL [19]
Peak power transfer eff. (%)	99.6	97	98	82.4	94
K_{12} at peak efficiency	0.1	0.7	0.7	N/A	0.2
R_L at peak efficiency (Ω)	75	5	200		N/A
Lowest power transfer eff. (%)	93	2	6		N/A
Var. in eff. from peak eff. (%)	-7	-95	-92		N/A
Input voltage V_{in} (V)		21.2		4.9	42
Resonance frequency (KHz)		150		300	N/A
Output power P_{out}	12 W	120 W	125 W	2 W	111 W

4. CONCLUSION

WPT faces efficiency challenges as the coupling coefficient fluctuates due to misalignment and changes in the transmitter-receiver distance during charging. Additionally, varying loading conditions can impact PTE and the stability of output current and voltage. To address these issues, a hybrid LC-LC compensation topology is introduced, which outperforms the series compensated SS and SP topologies. The LC-LC topology demonstrated superior PTE across a wide range of loading and coupling conditions, reaching 99.6% and consistently exceeding 95% efficiency. Frequency splitting led to a slight reduction in efficiency to 93% under large load resistance. In contrast, the SS and SP topologies achieved only 97% and 98% efficiency, respectively, with their performance dropping below 10% under significant load variations. These findings highlight the adaptability of the LC-LC topology in both strongly and loosely coupled WPT systems, demonstrating its effectiveness across a wide range of loading and coupling conditions. This study contributes to advancing WPT system performance, with future work including the design and analysis of various hybrid compensation topologies and comparative studies among them. Further research will also focus on optimizing these topologies for high-power applications and integrating dynamic tuning control strategies. Additionally, future studies will explore the development of compact WPT systems and multi-frequency, multi-load systems, which could enhance performance and applicability. Experimental validation and real-world testing of the fabricated design will be crucial to ensure its practical effectiveness and support widespread adoption in WPT applications.

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AUTHOR CONTRIBUTIONS STATEMENT

This journal uses the Contributor Roles Taxonomy (CRediT) to recognize individual author contributions, reduce authorship disputes, and facilitate collaboration.

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C : Conceptualization

M : Methodology

So : Software

Va : Validation

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R : Resources

D : Data Curation

O : Writing - Original Draft

E : Writing - Review & Editing

Vi : Visualization

Su : Supervision

P : Project administration

Fu : Funding acquisition

CONFLICT OF INTEREST STATEMENT

The authors state no conflict of interest.

INFORMED CONSENT

The study doesn't involve human participants, and therefore, informed consent is not applicable.

ETHICAL APPROVAL

This study didn't involve any human participants, animals, or sensitive data. As such, ethical approval was not required.

DATA AVAILABILITY

The authors confirm that the data supporting the findings of this study are available within the article [and/or its supplementary materials].

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